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conomiamankiw Â· بارك للجون ترش إن

Cartoon مجنون 2015.02.15 01:07. محتوى

Gay Â· 6334f1c1588 Â· 20MBS ساعات

Â· عناوين فريق. بدون مع نادا. Â·

R&NmmbSoft. الخاص. The present

invention relates generally to digital communications and, more particularly, to a method and apparatus for filtering intersymbol interference and estimating the channel impulse response. In high speed communications, there is a high

degree of sensitivity to intersymbol interference. Specifically, in the time domain, when several contiguous symbols are all transmitted together at a high clock rate, the received waveform is effectively the sum of all of the transmitted symbols. That is, there is no way of knowing which of the transmitted symbols has been received and which has not. The received waveform is, of course, the result of the continuous transmission of many symbols, each of which contributes a small component to the received waveform. In most cases, the signal is corrupted by noise, which also contributes to the received waveform.

One way to overcome intersymbol interference is to use a differential amplifier circuit. The differential amplifier circuit can subtract one signal from the other in the presence of noise, thus producing a waveform which is the difference between two nearly identical waveforms. However, a differential amplifier circuit, or any type of intersymbol interference compensation circuit, cannot be used at high data rates. This is because the transmission path is continually changing. As a result, the transmitted waveform will continually change. Since the received waveform is the result of the transmission of many pulses, the

waveform which results can and, more importantly, should change from pulse to pulse. This is in contrast to an intersymbol interference compensation circuit, which would have to switch from compensating the waveform in one direction to the other to compensate for the changing transmission path. The circuit would then have to begin compensating in the other direction, switch back, etc. This is not acceptable for high speed operations. Referring to FIG. 1, a system in accordance with the prior

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